



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

TSUJI et al.

Serial No: 09/982,335

Filed: October 18, 2001

For: SEMICONDUCTOR INPUT
PROTECTION CIRCUIT

Art Unit: 2811

Examiner: NADAV, ORI

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Mark R. Kendrick 10/18/2002

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Date

AMENDMENT AND ELECTION IN RESPONSE TO RESTRICTION REQUIREMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

In response to the Office Action dated August 20, 2002 in connection with the
above-identified application the period of response extended by one month, to October
20, 2002, by the attached petition and fee, please enter and consider the following
amendment and remarks.

IN THE CLAIMS:

Claims 1, 3, 4, 5, and 7 have been amended and claims 21 and 22 have been
added.

10/28/2002 AADDF01 00000003 09982335

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02 FC:1201

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